

FIG. 3

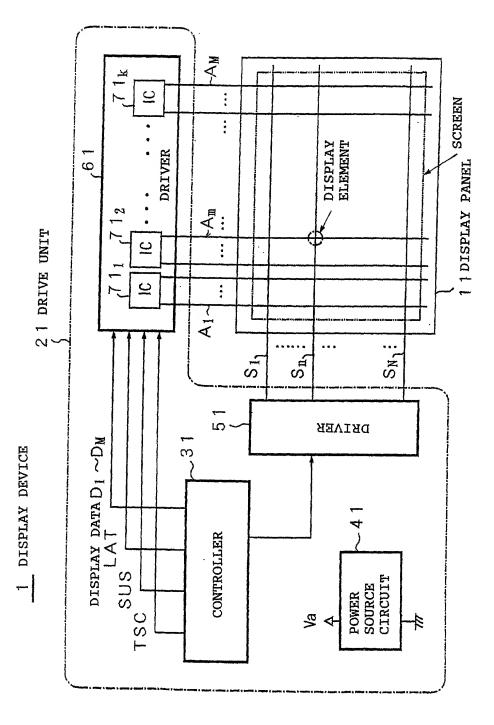
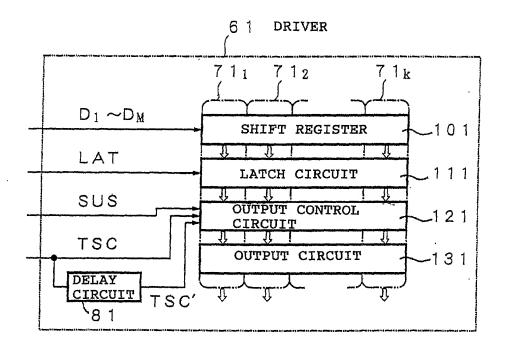


FIG. 4



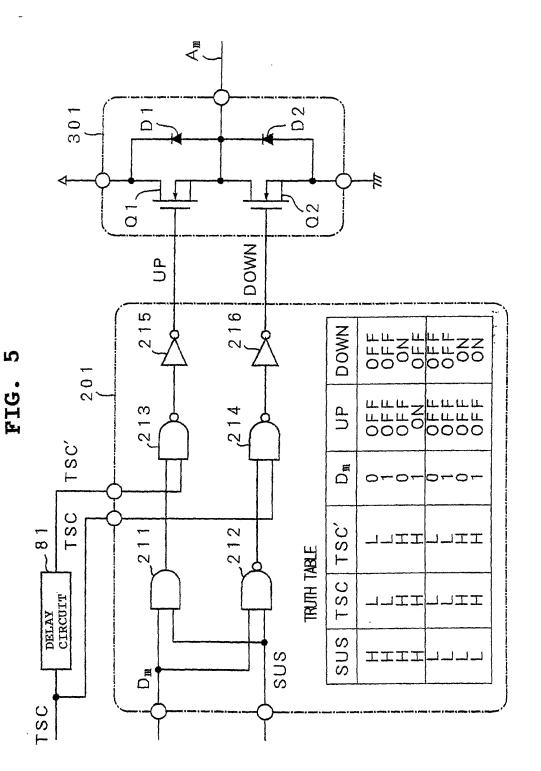
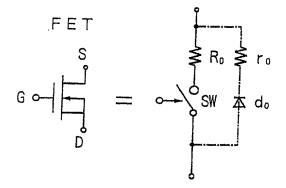
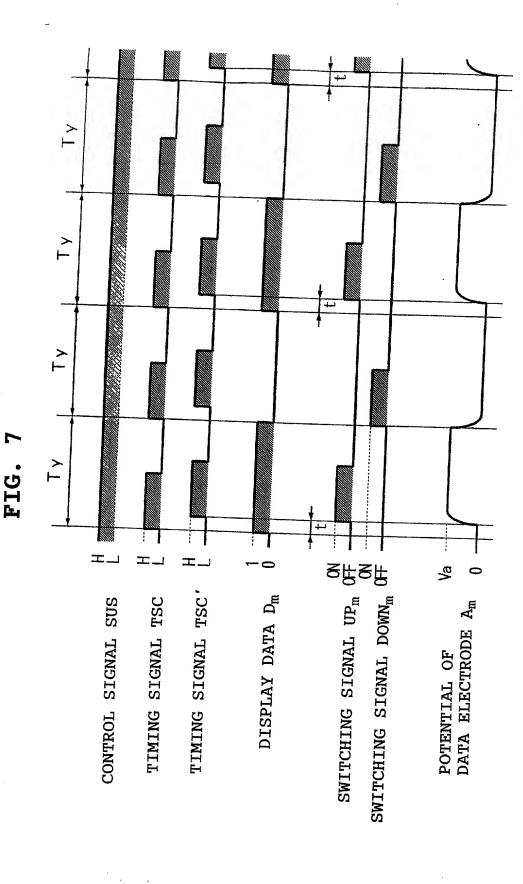
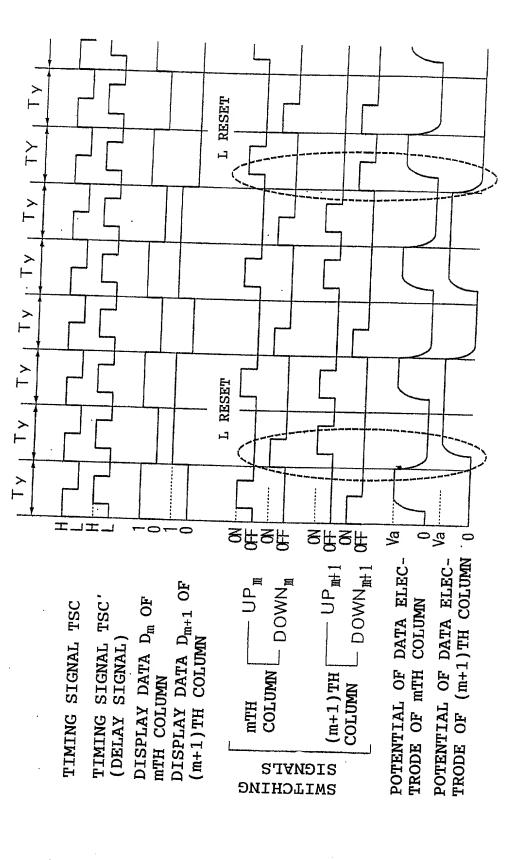


FIG. 6









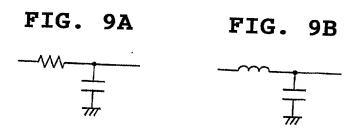
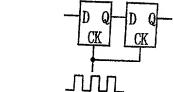


FIG. 9C FIG. 9D



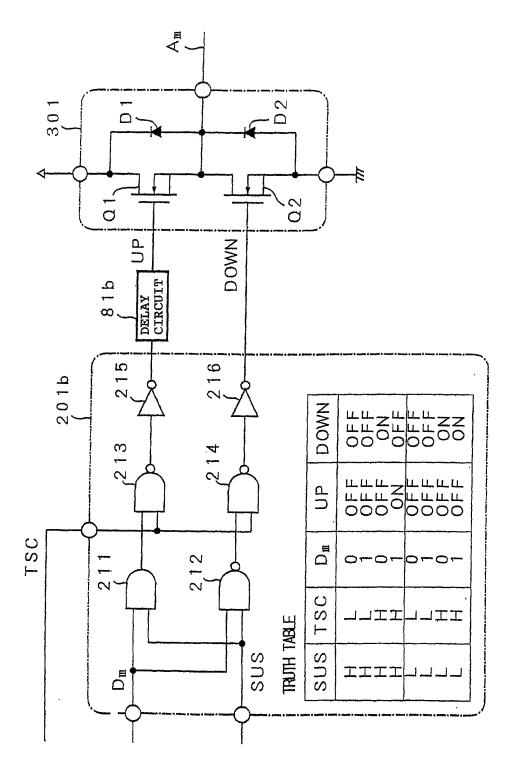
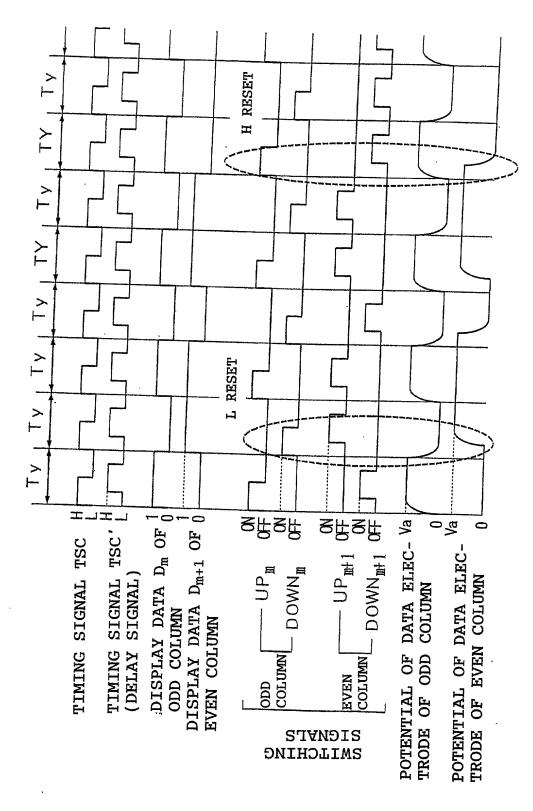


FIG. 10

F62B -12 .722k 62A  $\overline{\circ}$ : DISPLAY PANEL (72k+2 22 DRIVE UNIT 722 ೨ ပ  $^{-7}2$ kt 1 721 : ပ 2 DISPLAY DEVICE CIKCNIL DEFYX Dodd , Deven TSC' 82~ ITSC LAT CONTROLLER  $\sim$ POWER SOURCE CIRCUIT 32 ٧a

FIG. 11

FIG. 12



~103 13 -123 43 DRIVER AM  $A_{M-1}$ 63 DISPLAY PANEL OUTPUT CONTROL CIRCUIT LATCH CIRCUIT SHIFT REGISTER OUTPUT CIRCUIT A<sub>4</sub>  $A_3$ DISPLAY DEVICE  $A_2$  $A_1$ က -33 LAT SUS TSC D 1~DM CONTROLLER

FIG. 13

FIG. 14

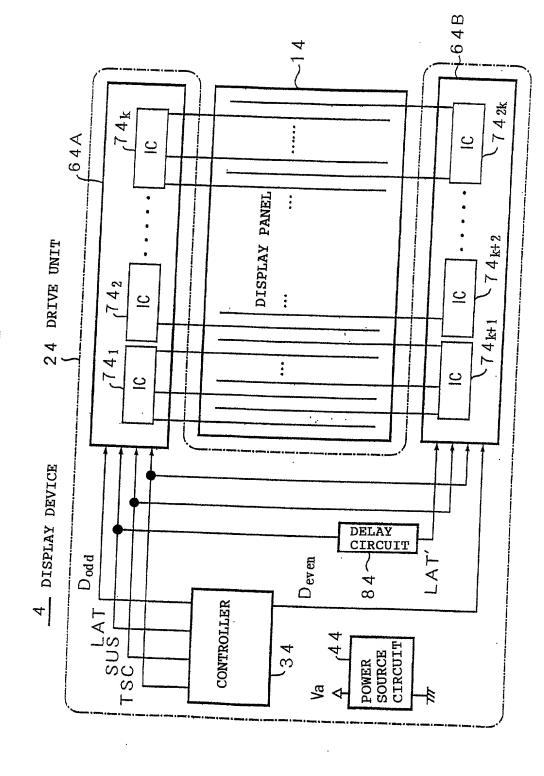


FIG. 15

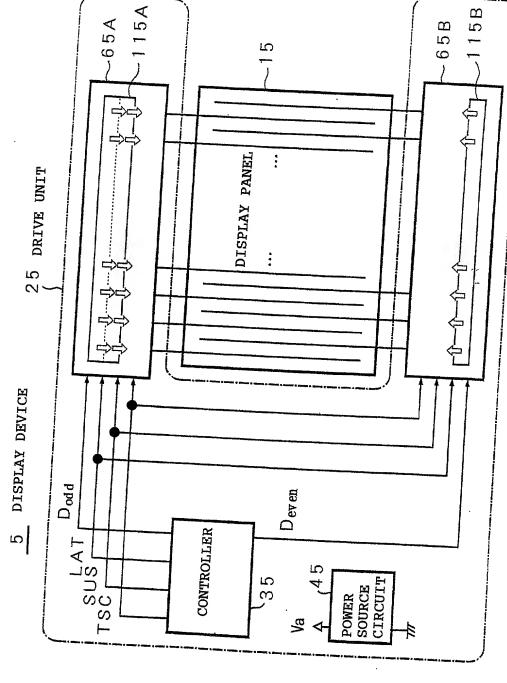


FIG. 16

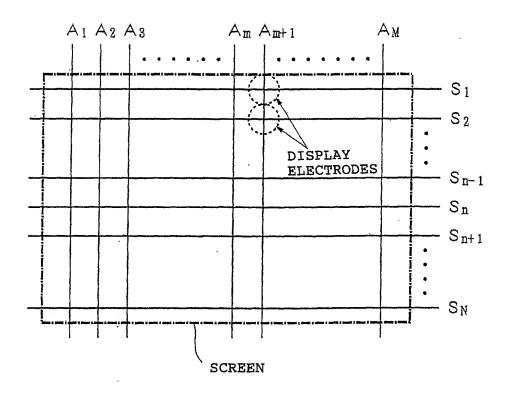
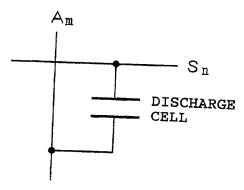


FIG. 17A

DISPLAY ELECTRODES OF PDP

## FIG. 17B

DISPLAY ELECTRODES OF PALC



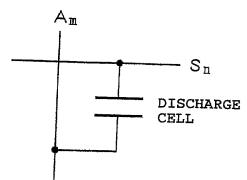
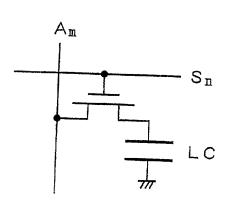


FIG. 17C

DISPLAY ELECTRODES OF LCD

FIG. 17D

DISPLAY ELECTRODES OF FED



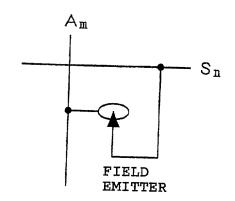


FIG. 18

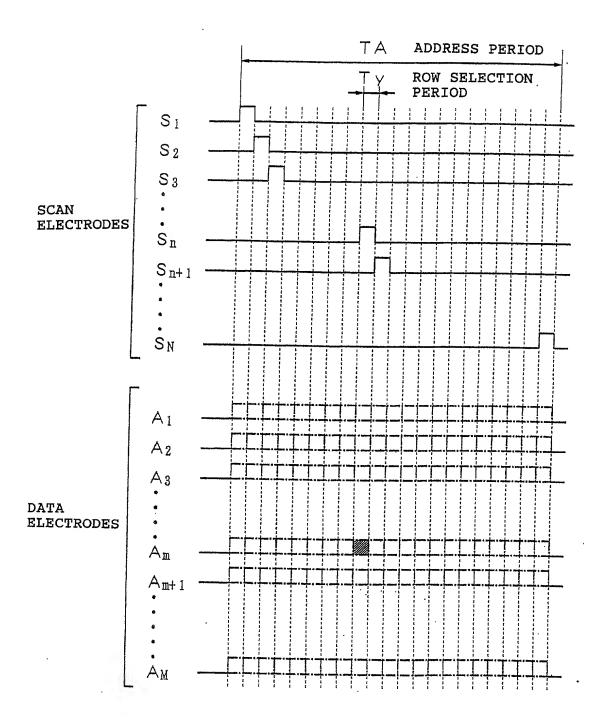


FIG. 19

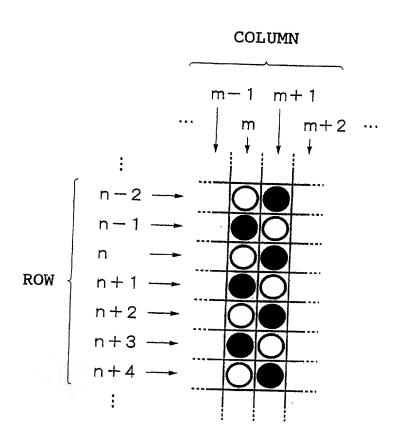


FIG. 20 Prior Art

